#### COMPLEMENTARY 30V ENHANCEMENT MODE MOSFET H-BRIDGE

#### **SUMMARY**

N-Channel =  $V_{(BR)DSS}$ = 30V :  $R_{DS(on)}$ = 0.12 $\Omega$ ;  $I_D$ = 3.1A P-Channel =  $V_{(BR)DSS}$ = -30V :  $R_{DS(on)}$ = 0.21 $\Omega$ ;  $I_D$ = -2.3A

#### **DESCRIPTION**

This new generation of trench MOSFETs from Zetex utilizes a unique structure that combines the benefits of low on-resistance with fast switching speed. This makes them ideal for high efficiency, low voltage, power management applications.

#### **FEATURES**

- · Low on-resistance
- · Fast switching speed
- Low threshold
- · Low gate drive
- Single SM-8 surface mount package

#### **APPLICATIONS**

• Single phase DC fan motor drive

#### **ORDERING INFORMATION**

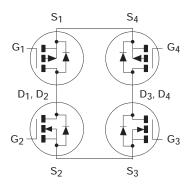
DEVICE	REEL SIZE	TAPE WIDTH	QUANTITY PER REEL
ZXMHC3A01T8TA	7″	12mm	1,000 units
ZXMHC3A01T8TC	13″	12mm	4,000 units

#### **DEVICE MARKING**

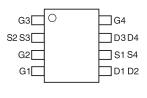
 ZXMH C3A01



SM8



#### **PINOUT**



Top View



#### **ABSOLUTE MAXIMUM RATINGS**

PARAMETER	SYMBOL	N-Channel	P-channel	UNIT
Drain-source voltage	$V_{DSS}$	30	-30	V
Gate-source voltage	V <sub>GS</sub>	±20	±20	V
Continuous drain current (V <sub>GS</sub> = 10V; T <sub>A</sub> =25°C) <sup>(b)(d)</sup>	I <sub>D</sub>	3.1	-2.3	А
(V <sub>GS</sub> = 10V; T <sub>A</sub> =70°C) <sup>(b)(d)</sup>		2.5	-1.8	А
$(V_{GS} = 10V; T_A = 25^{\circ}C)^{(a)(d)}$		2.7	-2.0	Α
Pulsed drain current (c)	I <sub>DM</sub>	14.5	-10.8	А
Continuous source current (body diode) (b)	I <sub>S</sub>	2.3	-2.2	А
Pulsed source current (body diode) (c)	I <sub>SM</sub>	14.5	-10.8	А
Power dissipation at T <sub>A</sub> =25°C <sup>(a) (d)</sup>	P <sub>D</sub>	1.3		W
Linear derating factor		10.4		mW/°C
Power dissipation at T <sub>A</sub> =25°C (b) (d)	P <sub>D</sub>	1.	W	
Linear derating factor		13	mW/°C	
Operating and storage temperature range	T <sub>j</sub> , T <sub>stg</sub>	-55 to	+150	°C

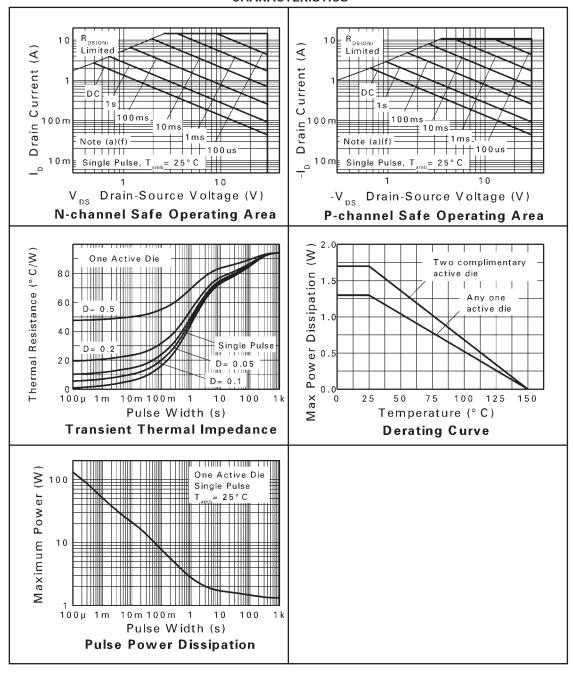
#### THERMAL RESISTANCE

PARAMETER	SYMBOL	VALUE	UNIT
Junction to ambient <sup>(a) (d)</sup>	$R_{\theta JA}$	96	°C/W
Junction to ambient (b) (d)	$R_{\theta JA}$	73	°C/W

- (a) For a device surface mounted on 50mm x 50mm x 1.6mm FR4 PCB with high coverage of single sided 2oz copper, in still air conditions.
- (b) For a device surface mounted on FR4 PCB measured at t ≤10 sec.
  (c) Repetitive rating on 50mm x 1.6mm FR4, D= 0.02, pulse width 300μS pulse width limited by maximum junction temperature. Refer to transient thermal impedance graph.
- (d) For device with one active die.



#### **CHARACTERISTICS**





#### N-channel

# **ELECTRICAL CHARACTERISTICS** (at T<sub>amb</sub> = 25°C unless otherwise stated)

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITIONS
STATIC						
Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	30			V	I <sub>D</sub> = 250μA, V <sub>GS</sub> =0V
Zero gate voltage drain current	I <sub>DSS</sub>			1.0	μΑ	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V
Gate-body leakage	I <sub>GSS</sub>			100	nA	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V
Gate-source threshold voltage	V <sub>GS(th)</sub>	1.0		3.0	V	I <sub>D</sub> = 250μA, V <sub>DS</sub> =V <sub>GS</sub>
Static drain-source on-state resistance <sup>(1)</sup>	R <sub>DS(on)</sub>			0.12 0.18	Ω	V <sub>GS</sub> = 10V, I <sub>D</sub> = 2.5A V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 2.0A
Forward transconductance (1) (3)	9 <sub>fs</sub>		3.5		S	V <sub>DS</sub> =4.5V, I <sub>D</sub> = 2.5A
DYNAMIC (3)				•		
Input capacitance	C <sub>iss</sub>		190		pF	V 25V V 0V
Output capacitance	C <sub>oss</sub>		38		pF	- V <sub>DS</sub> = 25V, V <sub>GS</sub> =0V - f=1MHz
Reverse transfer capacitance	C <sub>rss</sub>		20		pF	1 = 11011112
SWITCHING <sup>(2) (3)</sup>	•	•				
Turn-on-delay time	t <sub>d(on)</sub>		1.7		ns	
Rise time	t <sub>r</sub>		2.3		ns	V <sub>DD</sub> = 15V, I <sub>D</sub> = 2.5A
Turn-off delay time	t <sub>d(off)</sub>		6.6		ns	$R_G \cong 6.0\Omega$ , $V_{GS} = 10V$
Fall time	t <sub>f</sub>		2.9		ns	
Total gate charge	Qg		3.9		nC	V <sub>DS</sub> = 15V, V <sub>GS</sub> = 10V
Gate-source charge	Q <sub>gs</sub>		0.6		nC	I <sub>D</sub> = 2.5A
Gate drain charge	Q <sub>gd</sub>		0.9		nC	11D- 2.3A
SOURCE-DRAIN DIODE						
Diode forward voltage <sup>(1)</sup>	V <sub>SD</sub>			0.95	V	T <sub>j</sub> =25°C, I <sub>S</sub> = 1.7A, V <sub>GS</sub> =0V
Reverse recovery time <sup>(3)</sup>	t <sub>rr</sub>		17.7		ns	T <sub>j</sub> =25°C, I <sub>S</sub> = 2.5A,
Reverse recovery charge <sup>(3)</sup>	Q <sub>rr</sub>		13.0		nC	di/dt=100A/μs

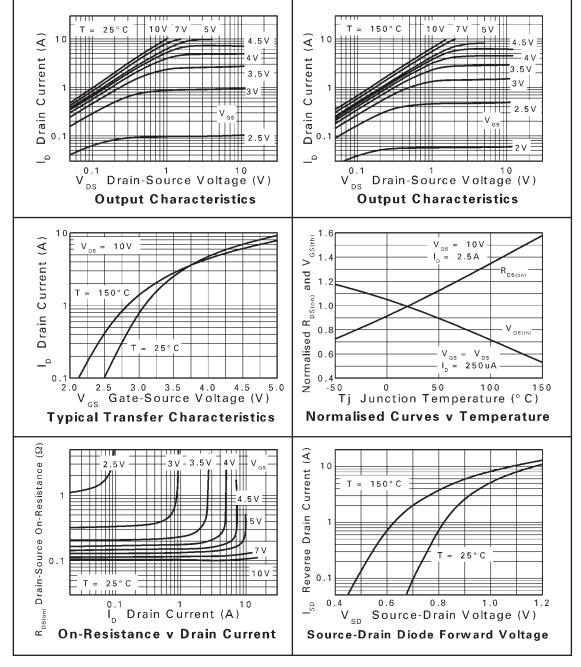
#### NOTES

- (1) Measured under pulsed conditions. Pulse width  $\leq 300 \mu s;$  duty cycle  $\leq 2\%.$
- (2) Switching characteristics are independent of operating junction temperature.
- (3) For design aid only, not subject to production testing.



#### N-channel

#### TYPICAL CHARACTERISTICS



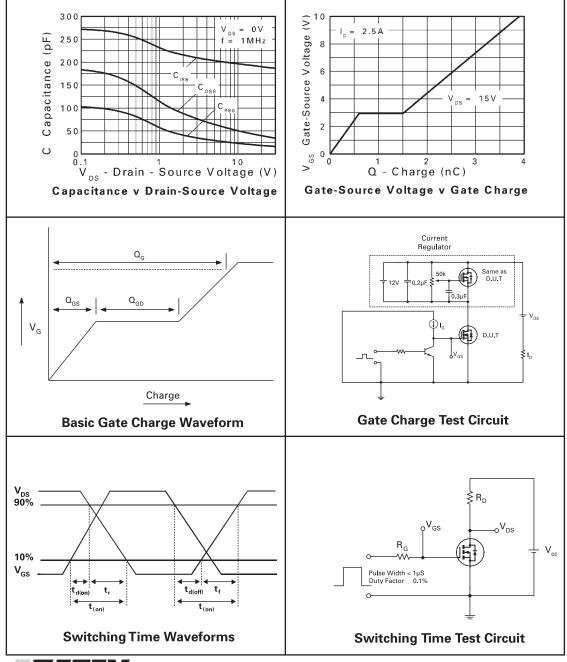
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#### N-channel

#### TYPICAL CHARACTERISTICS





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#### P-channel

# **ELECTRICAL CHARACTERISTICS** (at T<sub>amb</sub> = 25°C unless otherwise stated)

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITIONS
STATIC						•
Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	-30			V	I <sub>D</sub> = -250μA, V <sub>GS</sub> =0V
Zero gate voltage drain current	I <sub>DSS</sub>			-1.0	μΑ	V <sub>DS</sub> = -30V, V <sub>GS</sub> =0V
Gate-body leakage	I <sub>GSS</sub>			100	nA	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V
Gate-source threshold voltage	V <sub>GS(th)</sub>	-1.0		-3.0	V	I <sub>D</sub> = -250μA, V <sub>DS</sub> =V <sub>GS</sub>
Static drain-source on-state resistance <sup>(1)</sup>	R <sub>DS(on)</sub>			0.21	Ω	V <sub>GS</sub> = -10V, I <sub>D</sub> = -1.4A V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -1.1A
Forward transconductance (1) (3)	g <sub>fs</sub>		2.5		S	$V_{DS} = -15V, I_{D} = -1.4A$
DYNAMIC (3)						
Input capacitance	C <sub>iss</sub>		204		pF	V <sub>DS</sub> = -15V, V <sub>GS</sub> =0V
Output capacitance	C <sub>oss</sub>		39.8		pF	f=1MHz
Reverse transfer capacitance	C <sub>rss</sub>		25.8		pF	
SWITCHING <sup>(2) (3)</sup>	<u>,                                      </u>	•				,
Turn-on-delay time	t <sub>d(on)</sub>		1.2		ns	V <sub>DD</sub> = -15V, I <sub>D</sub> = -1A
Rise time	t <sub>r</sub>		2.3		ns	$R_G \cong 6.0\Omega$ , $V_{GS} = -10V$
Turn-off delay time	t <sub>d(off)</sub>		12.1		ns	
Fall time	t <sub>f</sub>		7.5		ns	
Total gate charge			2.6		nC	V <sub>DS</sub> = -15V, V <sub>GS</sub> = -5V I <sub>D</sub> = -1.4A
Total gate charge	Qg		5.2		nC	V <sub>DS</sub> = -15V, V <sub>GS</sub> = -10V
Gate-source charge	Q <sub>gs</sub>		0.7		nC	I <sub>D</sub> = -1.4A
Gate drain charge	Q <sub>gd</sub>		0.9		nC	
SOURCE-DRAIN DIODE	, ,	'				
Diode forward voltage <sup>(1)</sup>	V <sub>SD</sub>		-0.85	-0.95	V	T <sub>j</sub> =25°C, I <sub>S</sub> = -1.1A, V <sub>GS</sub> =0V
Reverse recovery time <sup>(3)</sup>	t <sub>rr</sub>		19		ns	T <sub>j</sub> =25°C, I <sub>S</sub> = -0.95A,
Reverse recovery charge <sup>(3)</sup>	Q <sub>rr</sub>		15		nC	di/dt=100A/μs

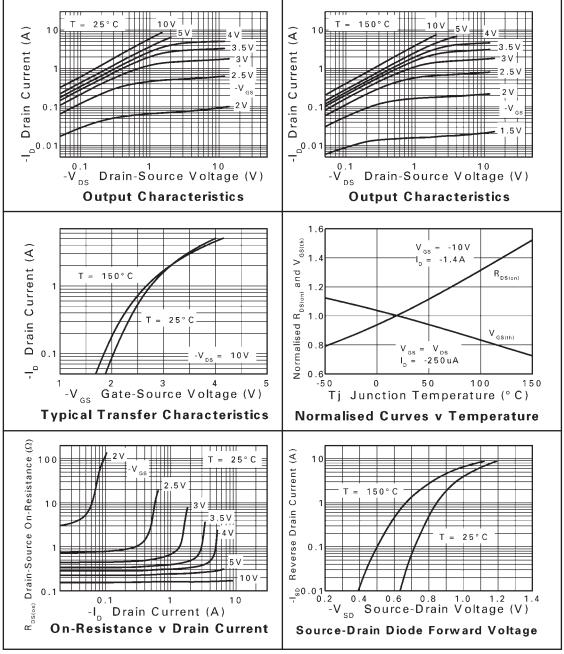
#### NOTES

- (1) Measured under pulsed conditions. Pulse width  $\leq 300 \mu s;$  duty cycle  $\leq 2\%.$
- (2) Switching characteristics are independent of operating junction temperature.
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#### P-channel

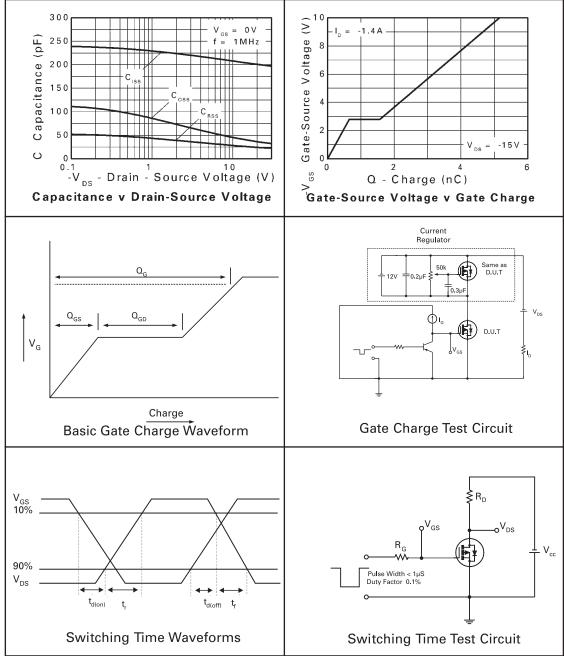






#### P-channel

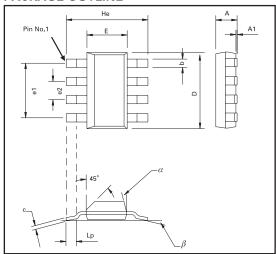
#### TYPICAL CHARACTERISTICS



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### **PACKAGE OUTLINE**



Controlling dimensions are in millimeters. Approximate conversions are given in inches

#### **PACKAGE DIMENSIONS**

DIM	Millimeters		Inches		DIM -	IV	lillimete	ers		Inches	;		
DIIVI	Min	Max	Тур.	Min	Max	Тур.	DIIVI	Min	Max	Тур.	Min	Max	Тур.
Α	-	1.7	-	-	0.067	-	e1	-	-	4.59	-	-	0.1807
A1	0.02	0.1	-	0.008	0.004	-	e2	-	-	1.53	-	-	0.0602
b	-	-	0.7	-	-	0.0275	Не	6.7	7.3	-	0.264	0.287	-
С	0.24	0.32	-	0.009	0.013	-	Lp	0.9	-	-	0.035	-	-
D	6.3	6.7	-	0.248	0.264	-	α	-	15°	-	-	15°	-
Е	3.3	3.7	-	0.130	0.145	-	β	-	-	10°	-	-	10°

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